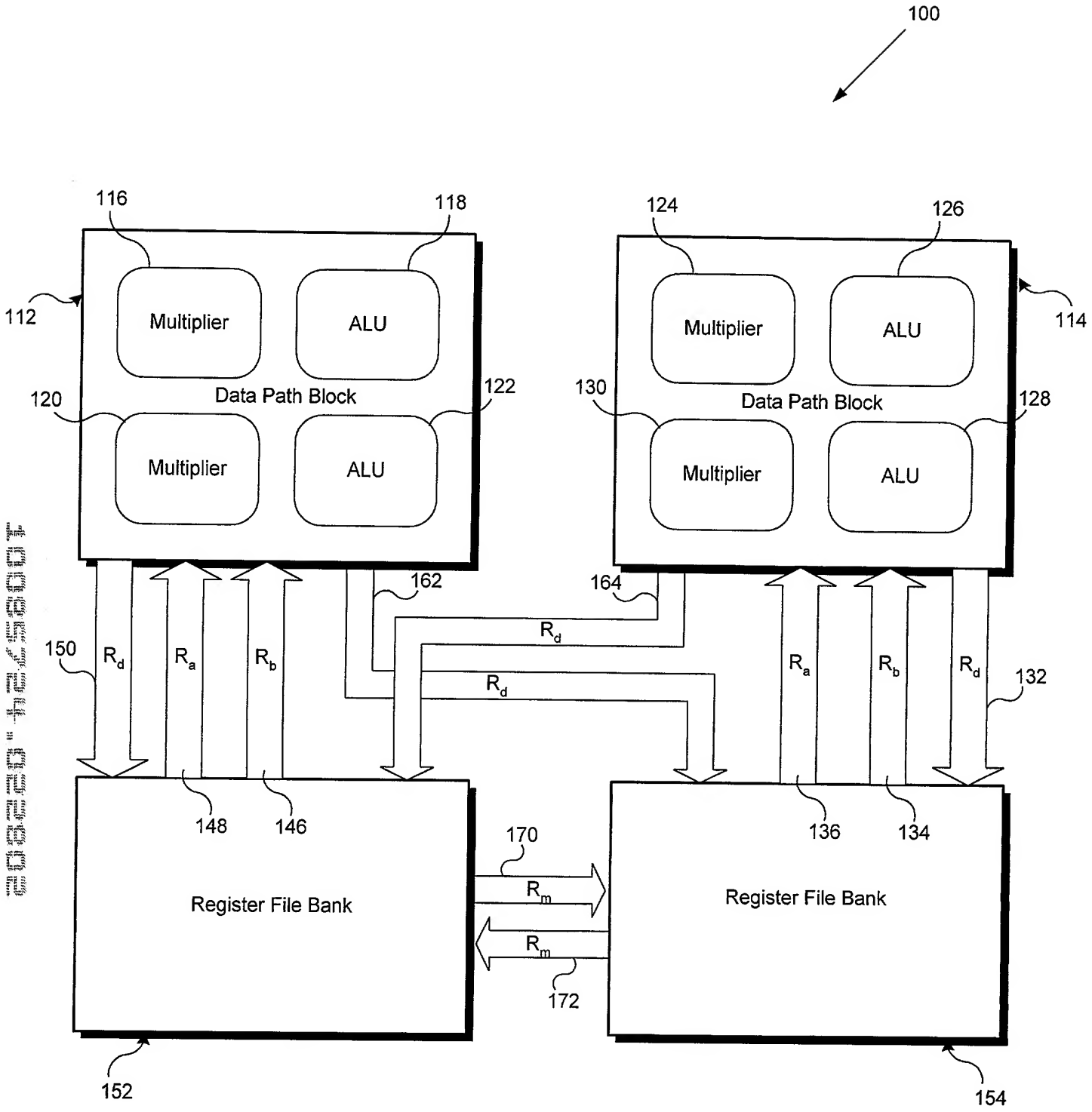

Figure 1
(Prior Art)

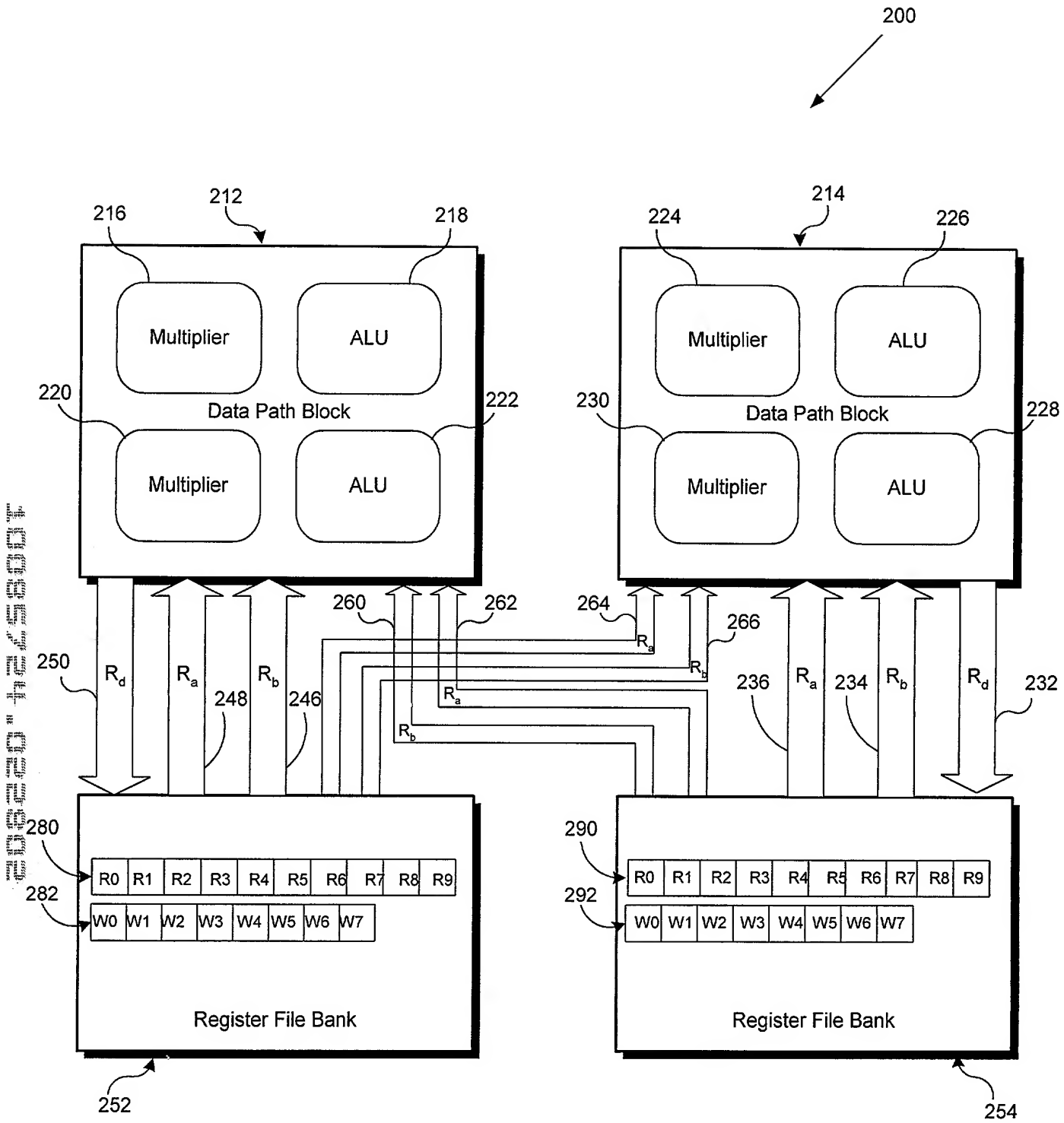
200
212
214
216
218
224
226
Multiplier
ALU
Data Path Block
220
222
230
228
260
262
264
266
250
248
246
236
234
232
280
290
R0 R1 R2 R3 R4 R5 R6 R7 R8 R9
282
292
W0 W1 W2 W3 W4 W5 W6 W7
Register File Bank
252
254

Figure 2

Port #	Execution Units		Bus	
R0	MULT 224 or ALU 226	MULT 216 or ALU 218	R_a 264	R_a 248
R1	MULT 224 or ALU 226	MULT 216 or ALU 218	R_b 266	R_b 246
R2	MULT 230 or ALU 228	MULT 220 or ALU 222	R_a 264	R_a 248
R3	MULT 230 or ALU 228	MULT 220 or ALU 222	R_b 266	R_b 246
310	312	314	316	318

Figure 3